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Inventors: Jack Hwang et al.

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Group Art Unit: 2822

Patent No.: 6,911,706

Issued: June 28, 2005

Examiner: M. Prenty

For: Forming Strained Source Drain
Junction Field Effect Transistors

Atty. Dkt. No.: ITL.0764D1US

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Attn: Decision and Certificate of Correction
Branch of the Patent Issue Division

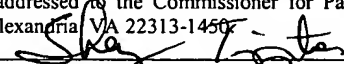
Certificate
JUL 28 2005
of Correction

**REQUEST FOR CERTIFICATE OF CORRECTION
OF PATENT FOR PTO MISTAKE
(37 C.F.R. § 1.322(a))**

1. Attached, in duplicate, is Form PTO-1050, with at least one copy being suitable for printing.
2. The exact page and line number where the error is shown correctly in the application file is in the "Reply to Paper No. 20050125", filed May 1, 2005, page 2, claim 22 (renumbered in the printed patent to claim 3), line 1.
3. Please send the Certificate to:

Timothy N. Trop
TROP, PRUNER & HU, P.C.
8554 Katy Freeway, Suite 100
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JUL 29 2005

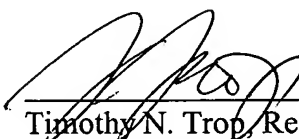
Date of Deposit: 7-22-05
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Sherry Tipton

4. All errors sought to be corrected were made in printing by the Patent and Trademark Office and no fee is believed to be due.

Respectfully submitted,

Date: _____

7/22/05



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UNITED STATES PATENT AND TRADEMARK OFFICE

CERTIFICATE OF CORRECTION

PATENT NO. : US 6,911,706 B2

DATED : June 28, 2005

INVENTOR(S) : Jack Hwang, Craig Andyke, Mitchell Taylor

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 4:

Line 11, after "3. The" insert --circuit--.

MAILING ADDRESS OF SENDER (Please do not use customer number

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PATENT NO. US 6,911,706 B2

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JUN 29 2005